

Remarks:

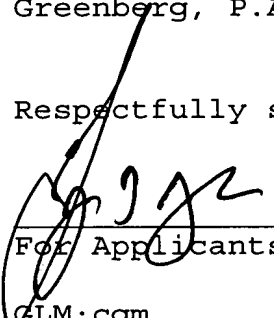
This preliminary amendment is being submitted in an effort to correct informalities and provide improved translations of certain wording in the initial application. The changes are not made for any reason related to the statutory requirements for a patent nor do they narrow the scope of the claim for any reason related to the statutory requirements for a patent. The newly entered claims are fully supported in the original claims.

Claim 8 has been amended to correct typographical errors.

An early action on the merits of the application is solicited.

Please charge any fees that might be due with respect to Sections 1.16 and 1.17 to the Deposit Account of Lerner and Greenberg, P.A., No. 12-1099.

Respectfully submitted,



For Applicants

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Version of Claims With Markings to Show Changes Made:

Claim 8 (amended). The circuit configuration according to claim 6, including an inverter chain (38), said detector (33) having one of:

an XOR gate (40) with XOR inputs, one of said XOR inputs receiving an [undelayed] edge signal (37), and another of said XOR inputs receiving [an] the edge signal (39) delayed through said inverter chain (38); and

an XNOR gate with XNOR inputs, one of said XNOR inputs receiving [an undelayed] the edge signal (37) and another of said XNOR inputs receiving [an] the edge signal (39) delayed through said inverter chain (38).